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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/386,034	08/30/1999	JEFFERY S. BECK	10980793-1	9943

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FORT COLLINS, CO 80527-2400

EXAMINER

LAMB, TWYLER MARIE

ART UNIT	PAPER NUMBER
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2622

DATE MAILED: 10/07/2003

5

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/386,034

Applicant(s)

BECK ET AL.

Examiner

Twyler M. Lamb

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 30 August 1999.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 16-19 is/are allowed.
- 6) ☒ Claim(s) 1 and 13-15 is/are rejected.
- 7) ☒ Claim(s) 2-12 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2, 4.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

### *Specification*

1. The abstract of the disclosure is objected to because it exceeds 150 words.

Correction is required. See MPEP § 608.01(b).

### *Claim Rejections - 35 USC § 102*

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1 and 13-15 are rejected under 35 U.S.C. 102(e) as being anticipated by Ishinaga et al. (Ishinaga) (US 6,290,334).

With regard to claim 1, Ishinaga discloses a printhead (semiconductor chip 1), comprising: a) a data receiver (input leads 17) and a data driver (driving integrated circuit), said data receiver and data driver being coupled to a common data pad (col 5, line 64 – col 6, line 18); b) a clock pad (clock pad 828; strobe pad 825); c) a data bus (wire bounding pads 45) (col 10, lines 15-18); d) a shift register (shift registers 11 and 27) comprising a plurality of storage elements chained between said data receiver and said data driver, wherein: i) at least one of said storage elements is coupled to said clock pad (col 10, lines 19-58); ii) outputs of a first number of said storage elements are coupled to said data bus (col 10, lines 15-18); and iii) a second number of said storage

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elements comprise first and second data inputs, of which said first data inputs form a part of said shift register, and said second data inputs are coupled to said data bus (col 10, lines 15-18); and e) a select line coupled to said second number of storage elements for switchably coupling either of said first or said second data inputs of said second number of storage elements to outputs of said second number of storage elements (col 10, lines 19-58).

With regard to claim 13, Ishinaga discloses a printhead, (semiconductor chip 1) comprising: a) a data receiver (input leads 17) and a data driver (driving integrated circuit), said data receiver and data driver being coupled to a common data pad (col 5, line 64 – col 6, line 18); b) a clock pad (clock pad 828; strobe pad 825); c) a data bus (wire bounding pads 45) (col 10, lines 15-18); d) means (shift registers 11 and 27) for shifting between said data receiver and said data driver in accordance with strobe signals received over said clock pad (col 10, lines 19-58); e) means for transferring data from said means for shifting to said data bus (col 10, lines 42-58); and f) means for transferring data from said data bus to said means for shifting (col 10, lines 42-58).

With regard to claim 14, Ishinaga also discloses wherein a) said means for shifting data is a means for shifting serial data; and b) said means for transferring data from said data bus to said means for shifting is a parallel data transfer means (col 10, lines 42-58).

With regard to claim 15, Ishinaga also discloses wherein a) said means for shifting data is a means for shifting serial data; and b) said means for transferring data

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from said means for shifting to said data bus is a parallel data transfer means (col 10, lines 42-58).

***Allowable Subject Matter***

4. Claims 2-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

5. Claims 16-19 are allowed.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Twyler Lamb whose telephone number is 703 - 308-8823. The examiner can normally be reached on M-TH (8:30-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward L Coles can be reached on 703-308-4712. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9306 for regular communications and 703-872-9314 for After Final communications.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, DC 20231

or faxed to:

(703) 872-9314

(for informal or draft communications, such as proposed amendments to be discussed at an interview; please label such communications "PROPOSED" or "DRAFT")

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or hand-carried to:

Crystal Park Two

2121 Crystal Drive

Arlington. VA.

Sixth Floor (Receptionist)

Twyler Lamb

A handwritten signature in black ink, appearing to be 'T. Lamb' or similar, written in a cursive style.

September 30, 2003